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Jang-Sik Lee received his B.S., M.S., and Ph.D. degrees in Materials Science and Engineering from Seoul National University, Korea in 1997, 1999, and 2002, respectively. In 2002, he joined the Los Alamos National Laboratory as a director's postdoctoral fellow. In 2004, he joined the memory division of Samsung Electronics as a senior research engineer and oversaw the integration and device reliability of 32 gigabit charge-trap flash memory. He was a professor at Kookmin University from 2006 to 2013. In 2013 he joined Pohang University of Science and Technology (POSTECH), Korea, where he is currently a Professor in the Department of Materials Science and Engineering and Mueunjae Chair Professor. His research interests include next-generation memory and neuromorphic devices using emerging materials and processes.